

# Modbus

## 参数地址编号分配

						DeviceNet ID		
1		WORD		R	0× 00	48	I/O	MCCB ACB VCB
2	L1	UINT	A	R	0× 01	21		
3	L2	UINT	A	R	0× 02	22		
4	L3	UINT	A	R	0× 03	23		
5	N	UINT	A	R	0× 04	25	I/O	ACB VCB
6	AB	UINT	V	R	0× 05	27		
7	BC	UINT	V	R	0× 06	28		
8	CA	UINT	V	R	0× 07	29		
9		UINT	A	R	0× 08	24		MCCB ACB VCB
10		INT	KW	R	0× 10	30		ACB VCB
11		SINT	%	R	0× 11	31		ACB VCB
12		UINT	0.1Hz	R	0× 12	49		ACB VCB
13		UINT	mA	R	0× 13	26		MCCB ACB VCB
14		WORD		R	0× 14	51		MCCB ACB VCB
15		UINT		R	0× 15	50		ACB VCB
16		WORD		R	0× 20	52		MCCB ACB VCB
17		UDINT	Ms	R	0× 21 0× 22	33		MCCB ACB VCB
18	L1	UINT	2A	R	0× 23	34		MCCB ACB VCB
19	L2	UINT	2A	R	0× 24	35		MCCB ACB VCB
20	L3	UINT	2A	R	0× 25	36		MCCB ACB VCB
21	N	UINT	2A	R	0× 26	53		MCCB ACB VCB
22		UINT	A	R	0× 27	54		MCCB ACB VCB
23		UINT	mA	R	0× 28	55		MCCB ACB VCB
24	AB	UINT	V	R	0× 29	37		ACB VCB
25	BC	UINT	V	R	0× 2A	38		ACB VCB
26	CA	UINT	V	R	0× 2B	39		ACB VCB
27	1	UINT	S	R/W	0× 30	14		ACB VCB
28	2	UINT	S	R/W	0× 31	15		ACB VCB
29	1	UINT	A	R/W	0× 32	12		ACB VCB
30	2	UINT	A	R/W	0× 33	13		ACB VCB
31	Ir	UINT	A	R/W	0× 34	16		MCCB ACB VCB

32	tr	UINT	S	R/W	0× 35	17		MCCB ACB VCB
33	Isd	UINT	A	R/W	0× 36	18		MCCB ACB VCB
34	tsd	UINT	mS	R/W	0× 37	19		MCCB ACB VCB
35	li	UINT	2A	R/W	0× 38	20		MCCB ACB VCB
36		WORD		R/W	0× 39	48		MCCB ACB VCB
37	Ig	UINT	A	R/W	0× 3A	25		MCCB ACB VCB
38	tg	UINT	MS	R/W	0× 3B	26		MCCB ACB VCB
39	N	UINT	A	R/W	0× 3C	27		ACB VCB
								MCCB
40		UINT	MA	R/W	0× 3D	29		MCCB ACB VCB
41		UINT	MS	R/W	0× 3E	30		MCCB ACB VCB
42		USINT		R/W	0× 3F	34		MCCB ACB VCB
43		USINT		R	0× 40	49		ACB VCB
				R/W				MCCB
				R/W				MCCB
44		WORD		W	0× 51	60		
					0× 70 — 0× 7F			
45		UINT		R	0× 90	50	260 V2.61	MCCB ACB VCB
46		UINT	V	R	0× 91	51		MCCB ACB VCB
47		UINT	V	R	0× 92	52		MCCB ACB VCB
48		UINT	A	R	0× 93	11		MCCB ACB VCB
49		UINT	A	R	0× 94	9		MCCB ACB VCB
50		USINT		R	0× 95	10		MCCB ACB VCB
51		SHORT — STRING	M[3102]	R	0× 96 — 0× 99	7	1 6	MCCB ACB VCB
52	CatNumber	SHORT — STRING		R	0× 9A — 0× A9	3	2 30	MCCB ACB VCB



Byte		Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
		█ 1= █ 0= █	█ 1= 0=	█ 1= 0=	█ 1= 0=	█ 1= 0=	█ 1= 0=	N 1= 0=	█ █ 1= 0=
		MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB

Byte		Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
		█ 1=ON 0=OFF	* █ 1=ON 0=OFF ( )	* █ 1=ON 0=OFF ( )	█ 1=ON 0=OFF	█ 1=ON 0=OFF	█ █ █ 1= 0=	█ █ █ 1=ON 0=OFF	█ █ █ 0= 1 1= 2
					MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB
					█ 1=ON 0=OFF	█ 1=ON 0=OFF	MCR █ 1=ON 0=OFF	N 00 OFF 01 50% 10 100% 11	
					MCCB ACB VCB	MCCB ACB VCB	MCCB ACB VCB	MCCB	

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### HST1

00H	█
01H	█
02H	█
03H	█
04H	█
05 0FH	█



3	63	0110 0011
	CRC	CRC 16

2

2

1	06H	MSB	LSB	MSB	LSB	LSB	MSB

1	06H	MSB	LSB	MSB	LSB	LSB	MSB

ModBus

RTU

1

8

2

1

128

1

1

1

1

CRC

2

01

02

03

05

06

CH2000